

Pf-2944/nec/us/mh

ABSTRACT OF THE DISCLOSURE

A stacked capacitor which comprises : a dielectric layer ; a two-
5 dimensional array of terminal electrodes on at least one of first and second
surfaces of the dielectric layer ; first internal electrodes stacked in multi-
levels in the dielectric layer, and the first internal electrodes being
electrically connected to a power line ; second internal electrodes stacked
in multi-levels in the dielectric layer, and the second internal electrodes
10 being electrically connected to a ground line ; vias in the dielectric layer, so
that the terminal electrodes being electrically connected through the vias to
the first and second internal electrodes.